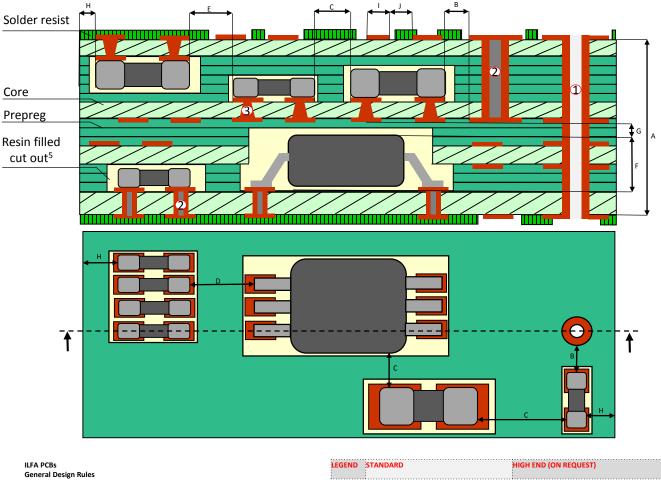


ILFA Designrules Embedding



LFA PCBs General Design Rules		LEGEND	STANDARD	HIGH END (ON REQUEST)
Max. PCB dimensions			420x570mm	On request
hickness multilayer	Depending on component heights	Α	0.8 - 4.2mm	On request
Metallized holes (details refer to the diameter of the irilling tool)				
Through hole		1	Aspect ratio 1:8, minimum Ø 100 μm	Aspect ratio 1:10, minimum Ø 100 μm
Through hole, Buried Via, Blind Via plugged and capped ¹ Standard microvia		2	Aspect ratio 1:8, minimum Ø 150 μm	Aspect ratio 1:10, minimum Ø 100 μm
	Copperfill on inner layer/ outer layer optional	3	Aspect ratio 1:1, minimum Ø 125 μm	Aspect ratio 1:1, minimum Ø 80 μm
Embedded components ^{2,3} (a BOM and a Pick&Place-List		, , , ,		
of all embedded components is nessesary)			> 500 um	On request
of all embedded components is nessesary) Distance component pad and via pad		В	≥ 500 µm	On request
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group)		B C	≥ 700 µm	On request
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group 4 to component or group	Maximum component tolerance is			On request ≥ 700 μm
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group to component or group Distance components on different layers		B C	≥ 700 µm ≥ 1mm ≥ 2 mm	On request
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group to component or group Distance components on different layers Component height	Maximum component tolerance is	B C	≥ 700 µm ≥ 1mm	On request ≥ 700 µm On request
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group to component or group Distance components on different layers Component height Space component and next layer	Maximum component tolerance is	B C D E F	≥ 700 μm ≥ 1mm ≥ 2 mm ≤ 1,6 mm	On request ≥ 700 μm On request ≤ 2,5 mm
of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group to component or group Distance components on different layers Component height Space component and next layer Distance component and board outline	Maximum component tolerance is	B C D E F	≥ 700 μm ≥ 1mm ≥ 2 mm ≤ 1,6 mm ≥ 250 μm	On request ≥ 700 μm On request ≤ 2,5 mm On request
Embedded components ^{2,3} (a BOM and a Pick&Place-List of all embedded components is nessesary) Distance component pad and via pad Distance component to component (not within group) Distance component group ⁴ to component or group Distance components on different layers Component height Space component and next layer Distance component and board outline Conductive pattern Trace width on inner & outer layers (µm)	Maximum component tolerance is	B C D E F	≥ 700 μm ≥ 1mm ≥ 2 mm ≤ 1,6 mm ≥ 250 μm	On request ≥ 700 μm On request ≤ 2,5 mm On request

¹Plugging is possible from a circuit board thickness of ≥0.3 mm excl. copper thickness. PCBs with external, flexible base materials, or materials without glass fabric cannot be plugged.

Furthermore, all ILFA design rules apply to multilayers.

²Components have be robust enough to sustain the embedding process.

³It is recommended to use IPC 7351B lmc (least material condition) = smallest possible pads or even better proportional landpattern design.

⁴A component group can be defined if the distance to the next components is < 700 μm. Each component of the group must face at least one outline of the cut out.

 $^{^5}$ Pregregs and cores will be cut out for the components. The clearance to the component is minimum 150 μ m. The outline of the cut out will be defined by ILFA.